

WHAT IS CLAIMED IS:

1. A plasma display panel comprising:
a back substrate;
electrodes each formed in a region partitioned;
barrier ribs arranged on said back substrate and
5 partitioning said region; and
a dielectric layer covering said electrodes,
wherein said barrier ribs and said dielectric layer
comprise the same barrier rib-forming material containing
a low melting point glass frit, and a film thickness of
10 said dielectric layer is in a range of 5 to 50 μm .
2. The plasma display panel according to claim 1,
wherein a film thickness of said dielectric layer is in the
range of 5 to 20 μm .
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3. A plasma display panel comprising:
a back substrate;
electrodes each formed in a region partitioned;
and
20 barrier ribs arranged on said back substrate and
partitioning said region,
wherein said barrier ribs are formed of a recessed
structure comprising a bottom structure contacting with
said back substrate, and an upper structure
25 projected from said bottom structure; and said
electrodes are each disposed at the bottom of said
recessed structure.

4. The plasma display panel according to claim 3, wherein the visible light reflectivity of the regions other than the electrodes is 50% or more under the condition where a phosphor is not coated.

5 5. The plasma display panel according to claim 3, wherein a recessed portion is formed at the bottom of the opening portion of the recessed structure, and the electrode is disposed at this recessed portion.

10 6. The plasma display panel according to claim 3, wherein the width of the recessed portion is the same in size as the bottom of the opening portion of the recessed structure.

15 7. The plasma display panel according to claim 3, wherein the thickness of the bottom of the recessed structure is larger than the width of the upper structure of recessed portion.

 8. A back plate of plasma addressed liquid
20 crystal display panel comprising:
 a back substrate;
 a transparent dielectric layer formed on said back substrate;
 transparent barrier ribs arranged on said
25 transparent dielectric layer and comprising the same material as that of said transparent dielectric layer;

an anode formed on said transparent dielectric layer; and

a cathode formed on said transparent dielectric layer;

5 wherein a film thickness of said transparent dielectric layer is in a range of 3 to 15 μm .

9. The back plate of plasma addressed liquid crystal display panel according to claim 8, wherein an
10 angle between a sidewall of the transparent barrier rib and said back substrate is in a range of 85 to 95 degrees.

10. The back plate of, plasma addressed liquid
15 crystal display panel according to claim 8, wherein a surface roughness of the sidewall of the transparent barrier rib is 1 μm or less and is equivalent almost to an optical flat surface.

20 11. A plasma display panel comprising:
a back substrate; and
barrier ribs arranged on said back substrate,
wherein said barrier ribs are formed of a recessed structure comprising a bottom structure contacting with
25 said back substrate, and an upper structure projected from said bottom structure; and visible light reflectivity of the back substrate is 50% or more under a condition where a phosphor is not coated.

12. A plasma display panel comprising:

a back substrate; and

barrier ribs arranged on said back substrate,

wherein said barrier ribs are formed of a recessed

5 structure comprising a bottom structure contacting with
said back substrate, and an upper structure projected from
said bottom structure; and a thickness of the bottom
structure of the recessed structure is larger than a width
of the upper structure of the recessed structure.

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